

a<sup>1</sup>

-- CIRCUIT CONFIGURATION FOR DETECTING THE CURRENT IN A LOAD  
TRANSISTOR --

Page 1, between lines 6 and 8, insert

a<sup>2</sup>

-- Background of the Invention:

Field of the Invention: --.

Page 1, lines 8-11, replace the paragraph reading:

"The present invention relates to a circuit arrangement having a load transistor and a current sensing transistor coupled to the load transistor in accordance with the features of the preamble of patent claim 1." with:

a<sup>3</sup>

-- The present invention relates to a circuit arrangement having a load transistor and a current sensing transistor coupled to the load transistor. --

\ Page 3, between lines 3 and 5, insert

a<sup>4</sup>

-- Summary of the Invention: --.

Page 3, lines 11-12, delete the paragraph reading, "This aim is achieved by means of a circuit arrangement in accordance with the features of patent claim 1."

Page 3, lines 14-15, delete the paragraph reading, "The subclaims relate to advantageous refinements of the invention."

Page 5, between lines 6 and 8, insert

A5  
-- Brief Description of the Drawings: --.

Page 5, lines 8-9, delete the paragraph reading, "The present invention is explained in more detail below using exemplary embodiments with reference to figures, in which:"

Page 5, between lines 20 and 22, insert

A6  
-- Description of the Preferred Embodiments: --.

Page 13, line 1, replace the paragraph reading, "Patent Claims" with

A7  
-- I Claim: --

Page 17, line 1, change the paragraph reading, "Abstract" to

A8  
-- Abstract of the Disclosure: --

Page 17, line, 12, delete the paragraph reading, "Figure 2".

Page 18, delete the entire page.